

cfcc

Docket No. 740819-1015

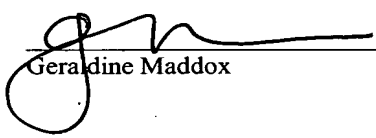


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
Yasuhiro SHIMADA et al. )  
Serial No. 10/626,722 )  
Patent No 6,967,859 B2 ) Art Unit: 2824  
Issued: November 22, 2005 ) Examiner: Van Thu T. Nguyen  
Filed: July 25, 2003 ) Confirmation No.  
For: Ferroelectric Memory Configuration Having ) Date: June 21, 2006  
Successively Connected Ferroelectric )  
Capacitor Coupling to the Gate of a Read )  
Transistor and Different Bias Voltages )  
Applied in Read/Write/Erase (As amended) )

Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 21, 2006.

  
Geraldine Maddox

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents  
Alexandria, VA 22313-1450

Certificate  
JUN 26 2006  
of Correction

Sir:

In reviewing the above-identified patent, printing errors were discovered therein requiring correction in order to conform the Official Record in the application.

The errors noted are set forth on the attached copy of Form PTO-1050 (Rev. 3-82) in the manner required by the Commissioner's Notice.

Specifically, upon reviewing the front page of the patent, it was noted that the inventor's are **NOT** listed in the correct order.

JUN 26 2006

**(75) Inventors:**

The inventor's should be listed as **-(1) Yasuhiro SHIMADA and (2) Yoshihisa KATO--;**

**Upon reviewing the claims:**

claim 3, column 23, line 29, "to said set" should read **--to said reset--;**

claim 4, column 23, line 60, "to said set line," should read **--to said set line;--;**

claim 4, column 23, line 64, "to said set line, and" should read **--to said set line; and--;**

claim 4, column 23, line 66, "exceed a conceive" should read **--exceed a coercive--;**

claim 5, column 24, line 2, "sub-step if removing" should read **--sub-step of removing--;**

claim 6, column 24, line 21, "to one of a" should read **--to one end of a--;**

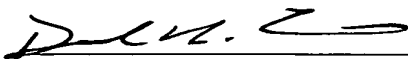
claim 6, column 24, line 22, "other and thereof" should read **--other end thereof--;**

claim 6, column 24, line 37, "and comparing with a reference voltage" should read **--and comparing, with a reference voltage--; and**

claim 6, column 24, line 38, "at both end" should read **--at both ends--.**

The change requested herein occurred as a result of printing the Letters Patent and the Certificate should be issued without expense under Rule 322 of the Rules of Practice. Accordingly, applicants request issuance of the Certificate of Correction.

Respectfully submitted,



Donald R. Studebaker  
Registration No. 32,815

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DRS/gm

2006

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO: 6,967,859 B2

DATED : November 22, 2005

INVENTOR(S) : Yasuhiro SHIMADA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**(75) Inventors:**

The inventor's should be listed as **-(1) Yasuhiro SHIMADA and (2) Yoshihisa KATO--;**

**What is claimed is:**

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MAILING ADDRESS OF SENDER:

PATENT NO. 6,967,859

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Suite 900, 401 9<sup>th</sup> Street, N.W.  
Washington, D.C. 20004-2128

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Alexandria, VA 22313-1450

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